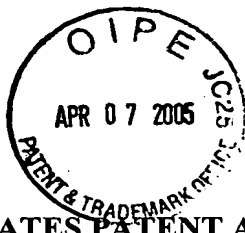


Docket No.: 250793US2S DIV



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Keiichi KUSHIDA

SERIAL NO.: 10/809,337

GAU: 2824

FILED: March 26, 2004

EXAMINER: LE, VU ANH

FOR: SEMICONDUCTOR MEMORY DEVICE

LETTER SUBMITTING REPLACEMENT DRAWING SHEET

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheet is submitted herewith:

☒ 1 Replacement Drawing Sheet ☐ \_\_\_\_\_ New Drawing Sheets

☒ Official Action dated JANUARY 7, 2005

☐ Notice of Allowance/Issue Fee dated \_\_\_\_\_

☐ Other dated \_\_\_\_\_

The changes and/or modifications made include the following:

A correction of  $V + \Delta V$  to  $V - \Delta V$  is made in Figure 9.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters  
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